Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet	of	

Complete if Known				
Application Number	10/812,242			
Filing Date	March 29, 2004			
First Named Inventor	Chad Thomas Steward			
Art Unit				
Examiner Name				
Attorney Docket Number	55123P298			

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T'		
Mr		GREBENE, ALAN B., "Bipolar and MOS Analog Integrated Circuit Design", 1984, Micro-Linear Corporation, Sunnyvale, California, Pgs. 853-855			
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Signature Blk War	Considered // / /	1/04
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 6 copy of this form with next communication.	09. Draw line through citation if not in o	onformance and not considered. Include

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450